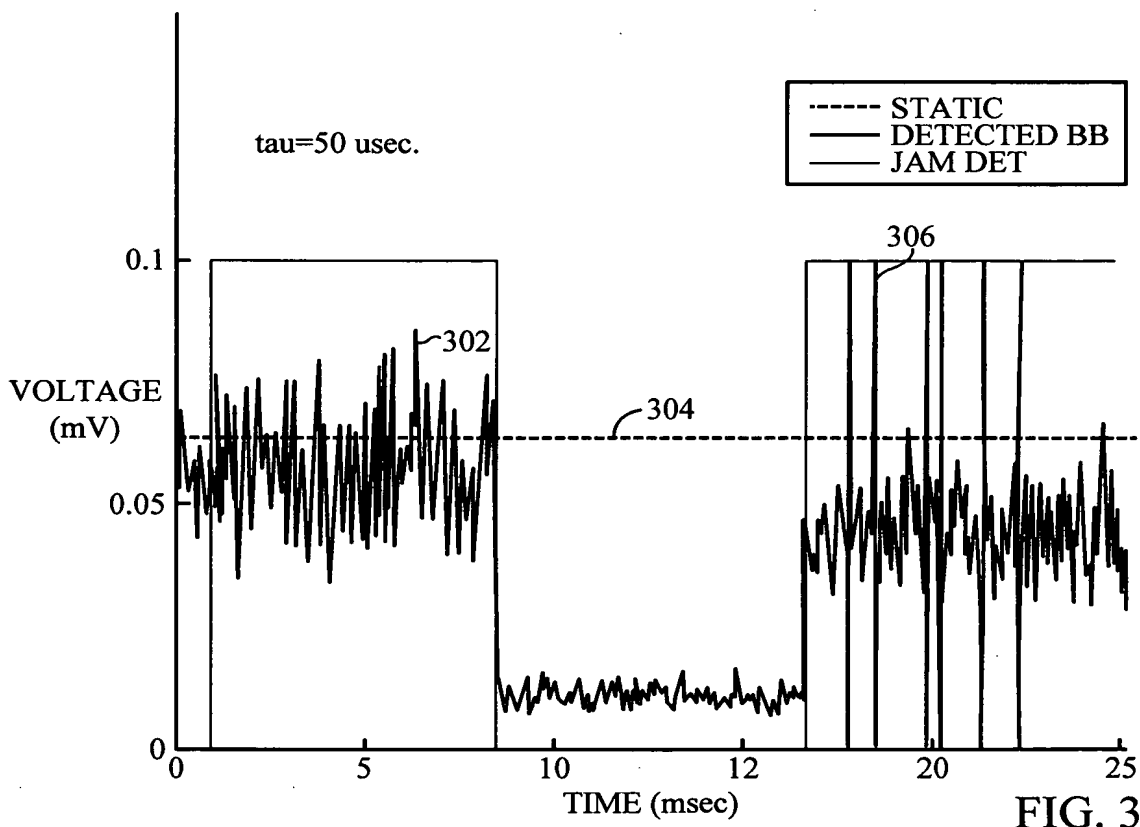
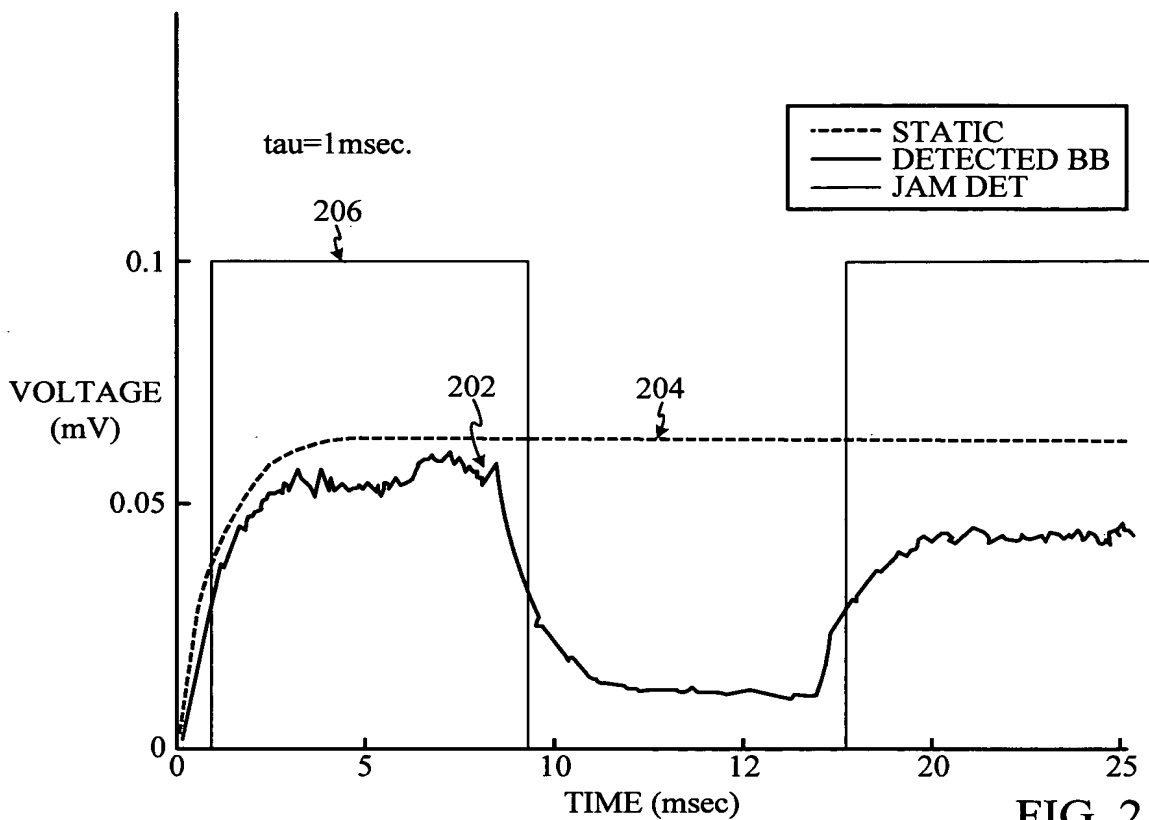


FIG. 1



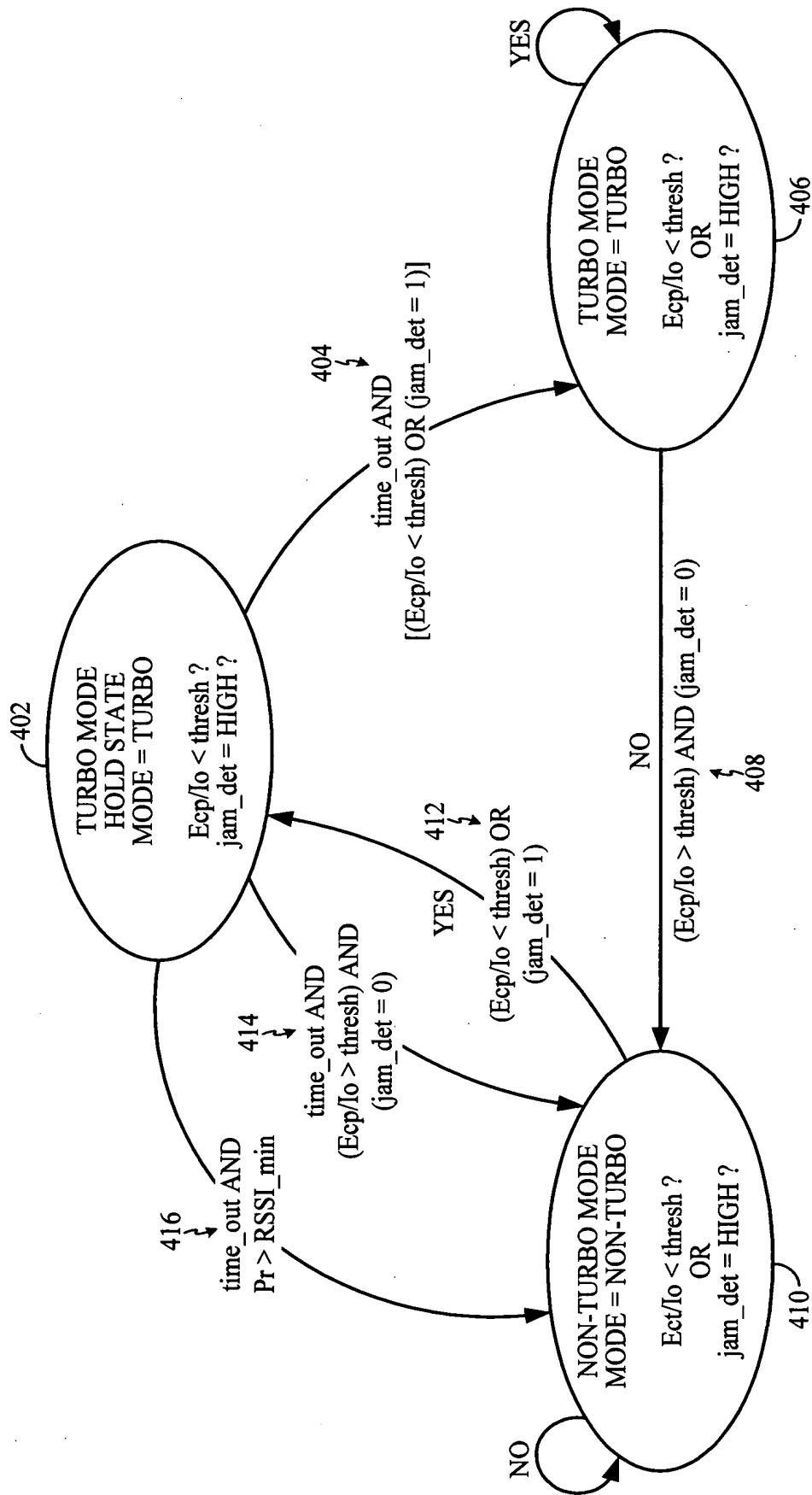


FIG. 4



502	UP BITS > 50%	504 Jam_det	506 TURBO MODE
508	0	0	0
510	0	1	1
512	1	0	1
514	0	1	1

FIG. 5



JAMMER DETECTOR	SIGNAL STRENGTH	Ecp/Io	LINEARITY	LPF STATE
OUTPUT (1-BIT A/D)	(RSSI)			
LOW	WEAK	X	LOW	MATCHED
LOW	STRONG	LOW	LOW	MATCHED
LOW	STRONG	HIGH	LOW	LOW ICI
HIGH	WEAK	X	HIGH	MATCHED
HIGH	STRONG	LOW	HIGH	MATCHED
HIGH	STRONG	HIGH	LOW	LOW ICI

FIG. 6

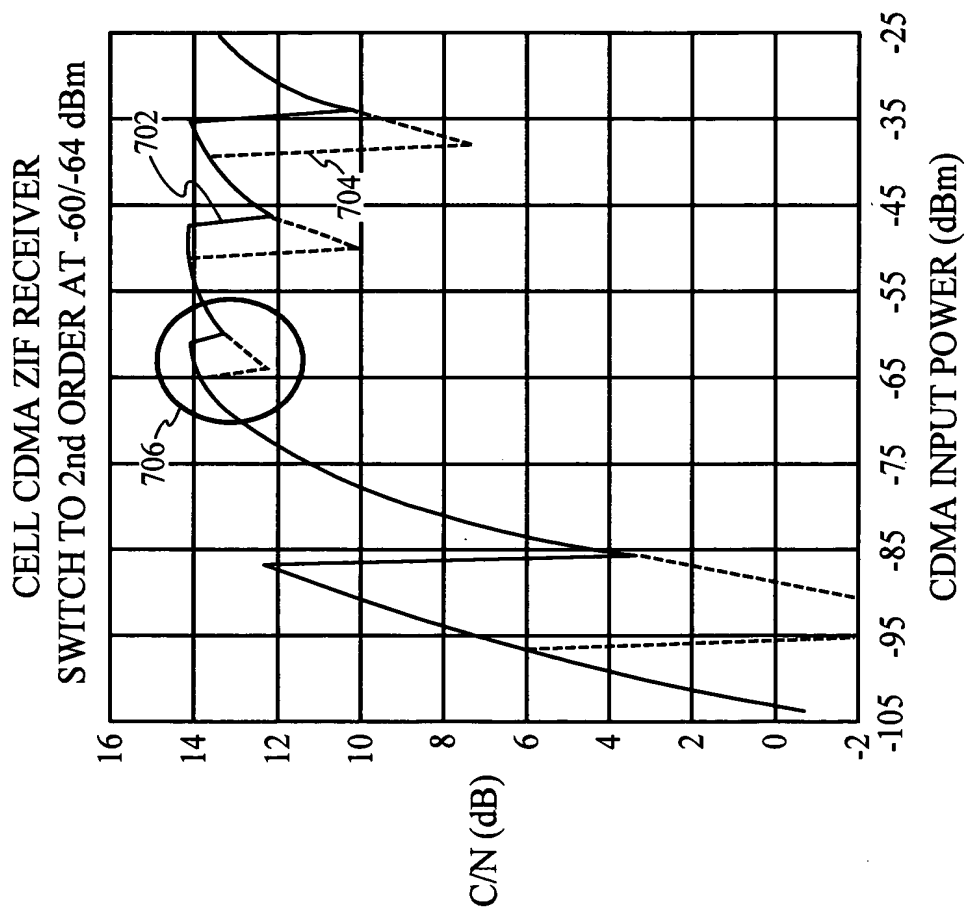


FIG. 7





800

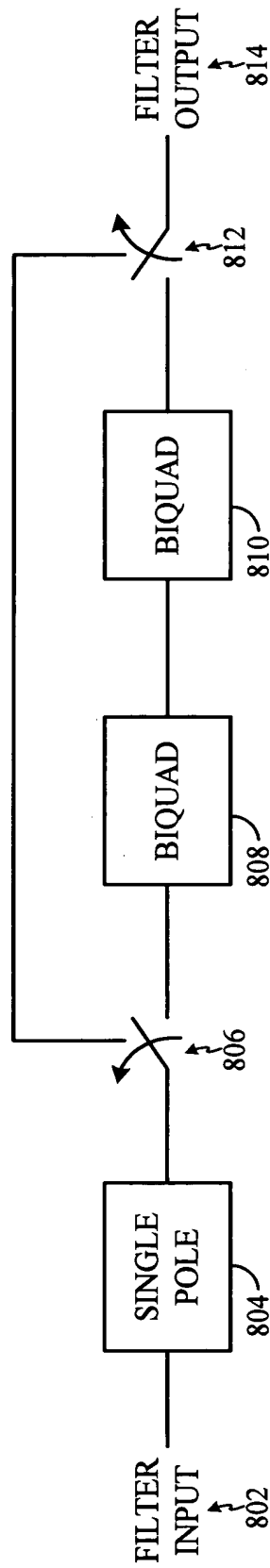


FIG. 8



FIG. 9